





Operating Manual-1001688-EN-10

- Decentralised system PSSuniversal I/O

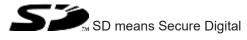


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Where unavoidable, for reasons of readability, the masculine form has been selected when formulating this document. We do assure you that all persons are regarded without discrimination and on an equal basis.

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1 Introduction

1.1 Validity of documentation

This documentation is valid for the product PSSu K F INC. It is valid until new documentation is published.

This operating manual explains the function and operation, describes the installation and provides guidelines on how to connect the product.

1.1.1 Retaining the documentation

This documentation is intended for instruction and should be retained for future reference.

1.1.2 Terminology: System environment A and B

The PSSu system can be used in two different system environments. The module's application area is described in the chapter "Intended Use" of the manual.

- The distinction is made between
- PSSu in system environment A
- PSSu in system environment B

The distinction is based on the application area of the PSSu system.

PSSu in system environment A may be used in the

- Decentralised system PSSu I/O
- Not in the automation system PSS 4000
- PSSu in system environment B may be used in the
- Automation system PSS 4000, e.g. with
 - Decentralised system PSSu I/O with SafetyNET p
 - Control system PSSu PLC
 - Control system PSSu multi

1.1.3 Names for sensor connections

The sensor connections have a a different designation depending on the manufacturer. An overview of the most frequent designation is shown in the following table.

Designation at Pilz	other designations	also possible
A+	Ua1+, Ua+	
A-	Ua1-, Ua-	
B+	Ua2+, Ua+	
В-	Ua2-, Ua-	
C+	Ua0+, Ua+	Z+, N+, R+
C-	Ua0-, Ua-	Z-, N-, R-
S	US, UaS	

The Pilz designation is used throughout this documentation.

1.2 Definition of symbols

Information that is particularly important is identified as follows:



DANGER!

This warning must be heeded! It warns of a hazardous situation that poses an immediate threat of serious injury and death and indicates preventive measures that can be taken.



WARNING!

This warning must be heeded! It warns of a hazardous situation that could lead to serious injury and death and indicates preventive measures that can be taken.



CAUTION!

This refers to a hazard that can lead to a less serious or minor injury plus material damage, and also provides information on preventive measures that can be taken.



NOTICE

This describes a situation in which the product or devices could be damaged and also provides information on preventive measures that can be taken. It also highlights areas within the text that are of particular importance.



INFORMATION

This gives advice on applications and provides information on special features.

2 Overview

2.1 Module structure

The module combines the function unit (inputs or outputs, interfaces) and connection levels in one housing.

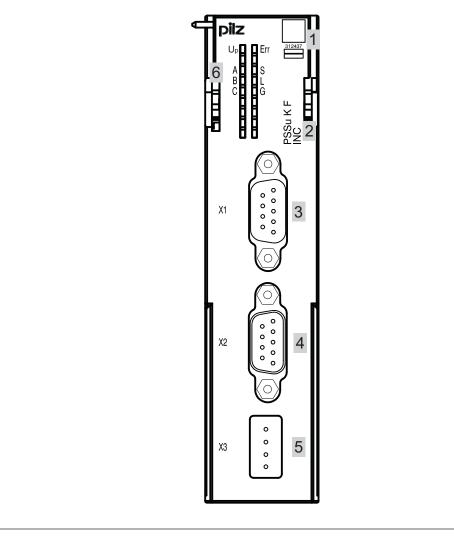
Wiring is via Sub-D female and male connectors

2.2 Module features

The product has the following features:

- Inputs for
 - Counter pulses (inputs A, B)
 - Zero pulse (input C)
 - Stopping the counter (input G, Gate)
 - Memory function (Input L, Latch)
 - Rotary encoder status (Input S, Status)
- Resolution of the counter and latch memory: 32 Bit
- Operating modes:
 - Incremental encoder
 - Counter
- Inputs A, B, C are operated as differential inputs with inverted signals (A-, B-, C-).
- LEDs for:
 - Data transfer per input A, B, C
 - Status per functional input (Gate, Latch, Status)
 - Module error
 - Sensor supply
- For failsafe and standard applications in system environment B (automation system PSS 4000)

2.3 Front view



Legend:

- ▶ 1: Labelling strip with:
 - 2D code
 - Order number
 - Serial number
 - Hardware version number
- > 2: Name of compact module
- 3: Female 9-pin D-Sub connector X1
- ▶ 4: Male 9-pin D-Sub connector X2
- ▶ 5: Male 4-pin connector X3
- ▶ 6: LEDs for status display and module diagnostics

3 Safety

3.1 Intended use

The module may be used for failsafe and standard applications in system environment B (automation system PSS 4000).

With the counter module you can implement the following functions:

Monitoring of:

- Position
- Speed
- Standstill

The module PSSu K F INC can be used as a PESSRAL (programmable electronic system in safety-related applications for lifts) in accordance with the Lifts Directive 2014/33/EU. It meets the requirements in accordance with EN 81-20, EN 81-50 for passenger and goods lifts and the requirements in accordance with EN 115-1 for escalators and moving walks.

Install the module/the safety controller in a protected environment. Example: Protected inside space or control cabinet with protection class and corresponding air conditioning specified in Technical details [44] 31].

Intended use includes making the electrical installation EMC-compliant. Please refer to the guidelines stated in the "PSSuniversal Installation Manual". The module is designed for use in an industrial environment. It is not suitable for use in a domestic environment, as this can lead to interference.

The following is deemed improper use in particular:

- Any component, technical or electrical modification to the module
- Use of the module outside the areas described in this manual
- Any use of the module that is not in accordance with the technical details.



INFORMATION

The module is supported by PAS4000 from Version 1.1.1. We recommend that you always use the latest version (download from www.pilz.de).

Special features for failsafe applications

To guarantee safety integrity for failsafe applications,

- 2 sensors and
- > 2 counter modules: a PSSu E F INC and a PSSu E F ABS SSI and
- the function blocks FS_IncrementalEncoder, FS_AbsoluteEncoder and FS_CounterDual must be used.
- ▶ For the block FS_CounterDual, the "single evaluation" option must be configured for the "Multiple evaluation" property in the module's properties view in PAS4000.

With redundant and diverse input values PL e and SIL CL 3 can be achieved.

To apply the module you will need to have read and understood the description of the function blocks (s. Online help). Requirements of the PSSu system:

- The counter modules must be part of a control system PSSu PLC or PSSu multi (not decentralised I/O system).
- The function blocks have to be assigned to the FS resource of the PSSu system where the counter modules are inserted. Control via SafetyNet is not permitted.

Demands on the sensors:

- Incremental encoders with RS422 interfaces may be used as sensors.
- Connect the sensors mechanically independently to the axis. If this is not possible, the connection should be positive-locking without wearing parts as e.g. chains or drive belts. If it is not possible to avoid this, the mechanical connection of the sensors must be mon-itored.

Alternatively, it is possible to use a sensor that internally has 2 independent measured value recording systems.

Please note during configuration and within the user program:

- > The input values must be diverse. This can be achieved through different
 - Scaling
 - Direction of rotation/count direction
 - Offset
- If more than two counters are used, each individual counter module of a PSSu system must supply diverse input values to the other counter modules of the PSSu system.
- Safe standstill monitoring must be carried out by feasibility with a safe travel command in the function block.
- > Test the safety functions when commissioning or recommissioning.

3.2 Safety regulations

3.2.1 Use of qualified personnel

The products may only be assembled, installed, programmed, commissioned, operated, maintained and decommissioned by persons who are competent to do so.

A competent person is a qualified and knowledgeable person who, because of their training, experience and current professional activity, has the specialist knowledge required. To be able to inspect, assess and operate devices, systems and machines, the person has to be informed of the state of the art and the applicable national, European and international laws, directives and standards.

It is the company's responsibility only to employ personnel who

- > Are familiar with the basic regulations concerning health and safety / accident prevention,
- Have read and understood the information provided in the section entitled Safety
- Have a good knowledge of the generic and specialist standards applicable to the specific application.

3.2.2 Warranty and liability

All claims to warranty and liability will be rendered invalid if

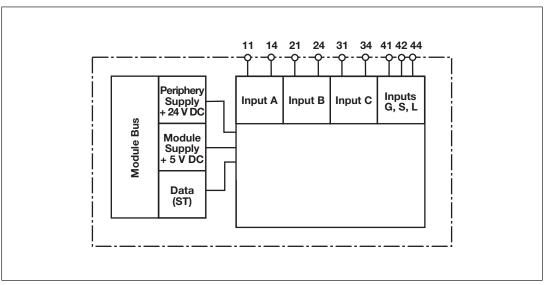
- > The product was used contrary to the purpose for which it is intended,
- Damage can be attributed to not having followed the guidelines in the manual,
- Operating personnel are not suitably qualified,
- Any type of modification has been made (e.g. exchanging components on the PCB boards, soldering work etc.).

3.2.3 Disposal

- ▶ In safety-related applications, please comply with the mission time T_M in the safety-related characteristic data.
- When decommissioning, please comply with local regulations regarding the disposal of electronic devices (e.g. Electrical and Electronic Equipment Act).

4 Function description

4.1 Block diagram



4.2 Module features

4.2.1 Integrated protection mechanisms

When the PSSu E F PS1(-T) or PSSu E F PS2(-T)(-R) is used to supply the system, the module supply is buffered for 20 ms if the supply voltage is interrupted.

The module detects the following errors:

- Start-up error
- Configuration error
- ST communication error
- Bus termination error

4.2.2 Function description

Module supply

> The module supply provides the module with voltage.

Inputs

- 3 dual-pole, differential inputs A, B, C for connecting an incremental encoder or an encoder that provides rising edges as counter pulses.
- 3 single-pole inputs referenced to earth: G, L, S, for special functions

Operating modes

- Incremental encoder
- Counter

Functions

Period length measurement

or

- Storing the counter status in latch memory after a latch pulse or zero pulse
- Setting the counter status

The module transfers the data and status information to the head module via the module bus. The choice of function and the function's configuration are defined via the system software.

4.2.2.1 Functional inputs (G, L, S)

The single-pole inputs (G, L, S) are used for special functions. Inputs G and L may be connected to external signal sources, e.g. to a higher order control system. If the gate signals are generated by a non-safety-related device or if the gate inputs are connected, common cause failures may arise. A failure mode and effects analysis must be carried out when wiring the gate inputs.

Input G (gate input)

The counter is stopped with a 1 signal. The module ignores the counter pulses at the inputs until a 0 signal returns.

Input L (input for latch pulse)

At a rising edge, the module stores the current counter value in the latch memory. The counter continues counting; it is not stopped by the latch pulse. The module transmits the stored value to the head module. The period length measurement may be configured as an alternative to the latch function.

Input S (status input)

The encoder's fault signal output can be connected to the status input. The module transmits the input state to the head module with the status information.

4.2.2.2 Overflow

In both operating modes the counter can accept values from $0000\ 0000_{H}$ to FFFF FFFF_H.

- ▶ With an underflow the value drops below 0000 0000_H and the counter continues from FFFF FFFF_H.
- With an overflow the value FFFF FFFF_H is exceeded and the counter continues from 0000 0000_H.

The overflow or underflow is signalled to the head module as status information.

The status information overflow is reset:

- ▶ if the value again falls below $0000 0000_{H}$ (underflow).
- ▶ if 5555 0000_H is exceeded (the lower third of the value range).

The status information underflow is reset:

- if FFFF FFFF_H is exceeded again (overflow).
- if AAAA $FFFF_{H}$ is exceeded (the upper third of the value range).

4.2.3 Incremental encoder operating mode

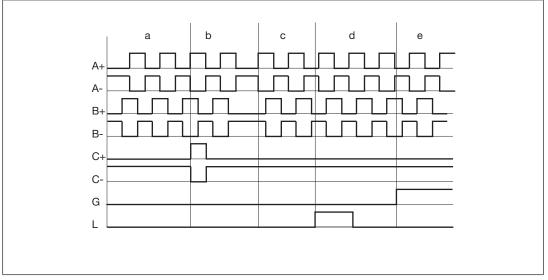
The counter outputs and the output for the incremental encoder's zero pulse are connected to the dual-pole inputs (A, B, C).

Inputs A, B

The first channel of the encoder is connected to input A, the second to input B. The second channel is 90° out of phase. If channel A is leading, the module counts forwards. If channel A is lagging, the module counts backwards (see timing diagram).

Input C

The output for the incremental encoder's zero pulse is connected to input C. An incremental encoder typically supplies one zero pulse per rotation. If the zero pulse function is activated, the module copies the last value prior to the zero pulse into the latch memory and passes it to the process image of inputs (see chapter entitled "Transfer counter status via latch pulse").



Legend:

- ▶ a: The counter counts backwards because the signal at channel A is lagging.
- b: The module has received a zero pulse. Provided the function is activated, the counter value is copied into the latch memory with a rising edge at input C+.
- > c: The counter counts forwards because the signal at channel A is leading.
- d: The module has received a latch pulse. Provided the function is activated, the counter value is copied into the latch memory with a rising edge at input L.
- e: The counter is disabled because there is a 1 signal at input G.

4.2.4 Counter operating mode

"Counter" operating mode is not recommended for safety functions because pulses can be generated if there are any track errors. Compare errors can arise as a result, which can lead to a shutdown or to common cause errors.

In "Counter" operating mode, the module's dual-pole, differential inputs A, B, C have the following functions:

Input A (Count)

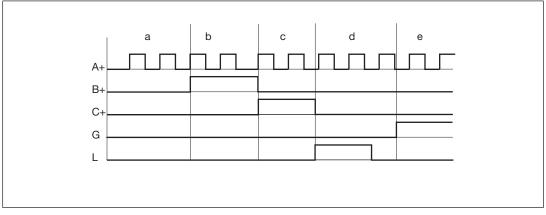
Input A is the input for the encoder's counter pulses. The module counts each rising edge.

Input B (Up/down)

At a 0 signal the module counts forwards. At a 1 signal the module counts backwards.

Input C (Gate/Latch)

The counter is stopped with a 1 signal. The module ignores the counter pulses at the input until a 0 signal returns.



Legend:

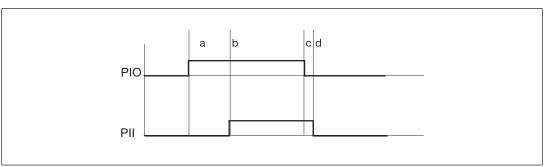
- ▶ a: The counter counts forwards because there is 0 signal at channel B.
- b: At the next rising edge at channel A, the counter counts backwards because there is a 1 signal at channel B.
- c: The counter is disabled because there is a 1 signal at input C.
- d: The module has received a latch pulse. Provided the function is activated, the counter value is copied into the latch memory with a rising edge at input L.
- e: The counter is disabled because there is a 1 signal at input G.

4.2.5 Functions

4.2.5.1 Measure period length

The module can record the period length of the counter pulses on channel A. The period length is the time between two rising edges at channel A. It is transferred to the process image of inputs as multiple of 200 ns.

Prerequisite: This function is configured in PAS4000.



Legend:

▶ PIO: Process image of outputs, I/O datum: OutputData.LatchOrMeasure

> PII: Process image of inputs, I/O datum: InputData.LatchOrMeasureDone

Key to	timing	diagram:
--------	--------	----------

Section	Function	Procedure for PSSu in system environ- ment B
а	Start measurement	In the user program, set OutputData.Lat- chOrMeasure
b	Output measured value	Measured value is written in InputData.Lat- chOrPeriod
	Set status bit	The module sets InputData.LatchOrMeas- ureDone
с	Finish measurement	In the user program, reset OutputData.Lat- chOrMeasure
d	Ready for new measure- ment	The module resets InputData.LatchOrMeas- ureDone

The result of the last period length measurement remains in the process image of inputs until the module signals a new measurement result by setting the status information. Before the initial measurement the process image of inputs contains $0000\ 0000_{\rm H}$ or FFFF FFFF_H.

The module issues the result of period length measurement in multiples of 200 ns.

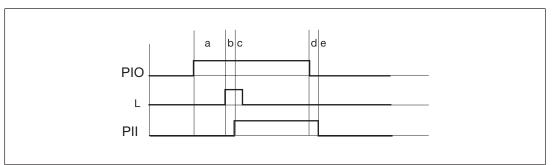
Example:

- The process image of inputs contains 32_H/50_D
- The period length is 200 ns x 50 = 10 μs

4.2.5.2 Transfer counter status via latch pulse

A signal output can be connected to input L on the module for a latch pulse. The latch pulse may come from a PLC or position switch, for example. Using the latch function it is possible to record and transmit the counter status at the time of this latch pulse.

Prerequisite: This function is configured in PAS4000.



Legend:

> PIO: Process image of outputs, I/O datum: OutputData.LatchOrMeasure

L: Input L for external latch

PII: Process image of inputs, I/O datum: InputData.LatchOrMeasureDone Key to timing diagram:

Section	Function	Procedure for PSSu in system environ- ment B
а	Activate latch function	In the user program, set OutputData.Lat- chOrMeasure
b	Fill latch memory	Rising edge at input L: Counter status is transferred to the latch memory
с	Output counter status	Counter status is written in InputData.Lat- chOrPeriod
	Set status bit	The module sets InputData.LatchOrMeas- ureDone
d	Finish latch function	In the user program, reset OutputData.Lat- chOrMeasure
е	Ready for new latch func- tion	The module resets InputData.LatchOrMeas- ureDone

The contents of the latch memory remains in the process image of inputs until the module signals a new memory value by setting the status information. Before the initial transfer the process image of inputs contains $0000\ 0000_{H}$ or FFFF FFFF_H.

The module always transmits the counter status when the first latch pulse occurs after the function has started. All subsequent latch pulses are ignored until the function is completed and reset.

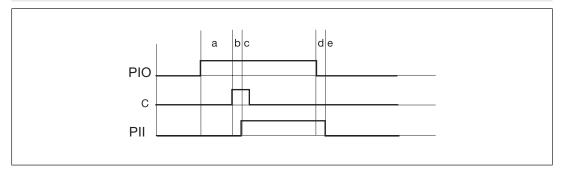
4.2.5.3 Transfer counter status via zero pulse

The output for the incremental encoder's zero pulse is connected to input C (C+/C-). An incremental encoder typically supplies one zero pulse per rotation. Using the zero pulse function it is possible to record the last counter status before the zero pulse and transmit it via the process image of inputs.



INFORMATION

In Counter operating mode, a rising edge at input C stops the counter.



Legend:

▶ PIO: Process image of outputs, I/O datum: OutputData.ZeroPulseActive

C: Input C

> PII: Process image of inputs, I/O datum: InputData.ZeroPulse

Key to timing diagram:

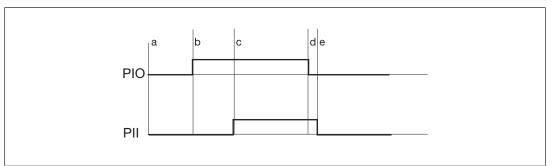
Section	Function	Procedure for PSSu in system environ- ment B
а	Activate zero pulse function	In the user program, set OutputDa- ta.ZeroPulseActiv
b	Fill latch memory	Rising edge at input C: Counter status is transferred to the latch memory
С	Output counter status	Counter status is written in InputData.Lat- chOrPeriod
	Set status bit	The module sets InputData.ZeroPulse
d	Finish zero pulse function	In the user program, reset Output- Data.ZeroPulseActiv
е	Ready for new latch func- tion	The module resets InputData.ZeroPulse

The zero pulse function has priority over the latch function and the "Period length measurement" function. If this function is activated, both the other functions are ignored, even if they have been activated.

The module always transmits the counter status when the first zero pulse occurs after the function has started. The counter statuses on all subsequent zero pulses are ignored until the function has been completed and reset.

4.2.5.4 Set counter status

The "Set counter status" function sets the counter to any value. The value is stated in the user program. The module transfers the value and continues counting from this counter status.



Legend:

▶ PIO: Process image of outputs, I/O datum: OutputData.SetCounter

▶ PII: Process image of inputs, I/O datum: InputData.SetCounterDone

Key to timing diagram:

Section	Function	Procedure for PSSu in system environ- ment B
а	Enter counter status	In the user program, assign the default counter status to OutputData.NewCounter- Value
b	Transfer counter status	In the user program, set OutputData.Set- Counter
С	Acknowledge transfer	The module sets InputData.SetCounter- Done; InputData.Underflow and Input- Data.Overflow are reset
d	Finish transfer	In the user program, reset OutputData.Set- Counter
е	Ready for new function	The module resets InputData.SetCounter- Done

4.2.5.5 Multiple evaluation

The module can evaluate the counter pulses once, twice or four times. Multiple evaluation increases the number of measuring steps and reduces the output frequency of the incremental encoder.

Multiple evaluation is only possible in incremental encoder operating mode.

Single evaluation option:

Each rising edge at channel A increases the counter status.

Double evaluation option:

Each rising and each falling edge at channel A increases the counter status.

• Quadruple evaluation option:

Each rising and each falling edge at channel A and channel B increases the counter status.

The **Double evaluation** and **Quadruple evaluation** option may not be used for failsafe applications.

4.3 Configuration

4.3.1 Operating modes and parameters

The module has the following configuration options:

Configuration	Default value	Meaning
Operating mode	Х	Incremental encoder mode
		Counter mode
Period length measure-	Х	Latch function
ment or latch function		Period length measurement
Multiple evaluation	Х	Single evaluation
		Double evaluation ⁽²⁾
		Quadruple evaluation ⁽²⁾
Status input	Х	The status at input S is transmitted via a bit.
		The status is transmitted via two redundant bits.
		The status is transmitted via two diverse bits. ⁽¹⁾

⁽¹⁾ Transmitting the status via two bits enables simple fault detection: Two redundant bits must always be the same, two diverse bits must always be different, otherwise the transmission is faulty.

⁽²⁾ The **Double evaluation** and **Quadruple evaluation** options may not be used for failsafe applications.

4.3.2 Input/output data

4.3.2.1 PSSu assignment in system environment B

Data access is via pre-defined I/O data types:

I/O data name	I/O data type	I/O data element	Meaning
OutputData	FS_O_INC	ZeroPulseActiv: BOOL	FALSE: Input C (zero pulse) inact- ive TRUE: Input C (zero pulse) active
		LatchOrMeasure: BOOL	FALSE: Input L (latch pulse) inact- ive/period length measurement in- active
			TRUE: Input L (latch pulse) active/ period length measurement active
		SetCounter: BOOL	FALSE: Do not transfer default counter status
			TRUE: Transfer default counter status
		NewCounterValue: DWORD	Default counter status

I/O data name	I/O data type	I/O data element	Meaning
InputData	FS_I_INC	CurrentData: DWORD	Current counter status in incre- mental encoder or counter mode
		LatchOrPeriod: DWORD	Counter status after a latch or zero pulse or period length
		ZeroPulse: BOOL	FALSE: No zero pulse at input C
			TRUE: Zero pulse at input C
		LatchOrMeasure- Done: BOOL	FALSE: Period length or contents of latch memory not transferred
			TRUE: Period length or contents of latch memory transferred
		SetCounterDONE: BOOL	FALSE: Default counter status not transferred
			TRUE: Default counter status trans- ferred
		Underflow: BOOL	FALSE: No counter underflow
			TRUE: Counter underflow
		Overflow: BOOL	FALSE: No counter overflow
			TRUE: Counter overflow
		State1: BOOL	FALSE: Status input, Bit 1 (mes- sage from encoder)
			TRUE: Status input, Bit 1
		State2: BOOL	FALSE: Status input, Bit 2 ⁽¹⁾
			TRUE: Status input, Bit 2

⁽¹⁾ When configuring the module, users can determine the evaluation method for the status input: single, redundant or diverse. Transmitting the status via two bits enables simple fault detection: Two redundant bits must always be the same, two diverse bits must always be different, otherwise the transmission is faulty.

5 Installation

5.1 General installation guidelines

Please refer also to the PSSuniversal Installation Manual.



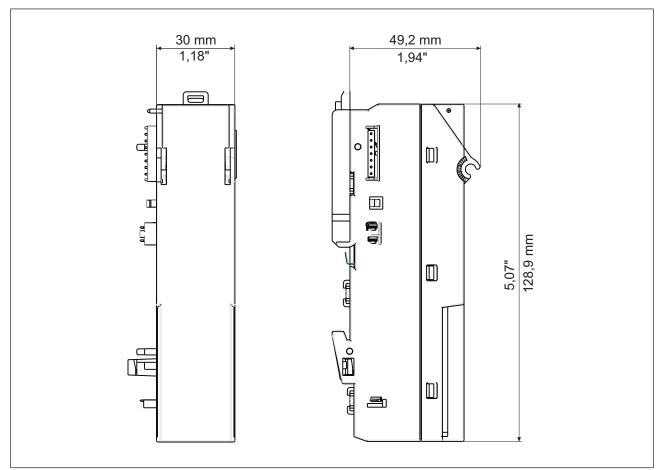
NOTICE

Damage due to electrostatic discharge!

Electrostatic discharge can damage components. Ensure against discharge before touching the product, e.g. by touching an earthed, conductive surface or by wearing an earthed armband.

5.1.1 Dimensions

Schematic representation:



5.2 Install compact module

Prerequisite:

- The head module must be installed.
- If the head module does not have an integrated power supply, a supply voltage module must be installed to the right of the head module.
- A base module with screw terminals may not be installed to the left of the compact module.

Please note:

- All contacts should be protected from contamination.
- > The mechanics of the compact modules are designed for 50 plug in/out cycles.

Procedure:

- Slot the groove on the compact module on to the mounting rail from below [1].
- > Push the compact module back as far as it will go [2].
- Make sure that the locking mechanism [3] is pushed downwards, connecting the module firmly to the mounting rail.
- > On the mounting rail, slide the compact module to the left.



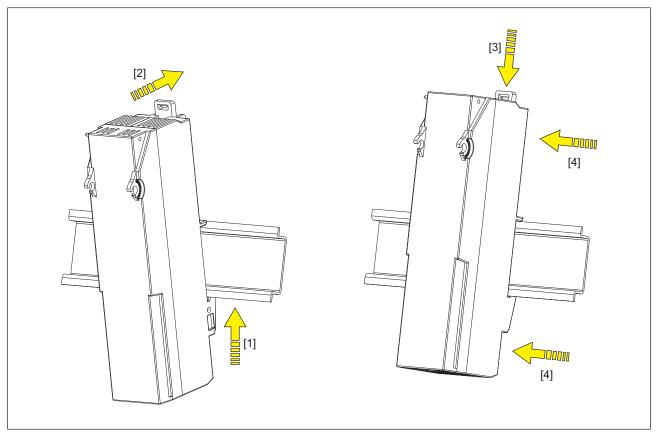
NOTICE

Potential contact damage due to twisting!

The contacts for the Module Supply and Periphery Supply can be bent by twisting the compact modules on the mounting rail.

 On the mounting rail, carefully slide the compact module to the left, in parallel to the adjoining module, until you hear the lateral mounting hooks on the adjacent module lock into position [4].





6 Wiring

6.1 General wiring guidelines

Please note:

- ▶ The module's connections are galvanically isolated from the module supply and periphery supply.
- ▶ For EMC reasons we recommend that you operate inputs A, B, C exclusively as differential inputs with inverted signals (A+/A-, B+/B-, C+/C-).
- ▶ The module evaluates open differential inputs (A+/A-, B+/B-, C+/C-) as a 1 signal.
- > The module evaluates open function inputs (G, L, S) as a 0 signal.
- Use twisted pair cables to carry the inverted signals. This will increase the noise immunity.
- Use shielded signal cables with metallic plugs. The screening should be connected to the housing of the D-Sub connector.
- The channel for the incremental encoder's zero pulse has a different designation depending on the manufacturer (N, C, Z, 0,...)
- The power supply must meet the regulations for extra low voltages with protective electrical separation (SELV, PELV).
- Use copper wiring.
- Connect the two sensors via separate cables. This also applies to compact encoders.
- Counter modules with low input signals detect even very small signal changes. In a particularly disturbed environment it can happen that signal changes caused by interferences are also detected.

In these cases, we recommend the following measures:

- Earth the mounting rail on both sides of the modules.
- Earth the shield connection of the cables on both sides (on the sensor/encoder and either on the module or directly on the point where the cable enters the control cabinet).
- Ensure that no transient currents flow across the cable screening, which may damage the cables or the connectors.

6.2 Interface configuration

Connection encoder sig- Layout				
nals				
Female 9-pin D-Sub con-	X1	\bigcirc		
nector	1: C+			
	2: B+	9		
	3: A+			
	4: n. c.	6		
	5: 0 V counter	lõl		
	6: C-			
	7: B-			
	8: A-			
	9: U_p (+5 V for sensor)			
Connection control	Layout			
Male 9-pin D-Sub connector	X2			
	1: C+			
	2: B+	6		
	3: A+			
	4: n. c.	9 5		
	5: 0 V counter			
	6: C-			
	7: B-			
	8: A-			
	9: U_p (+5 V for sensor)			
Connection functional out-	Lavout			
puts input devices				
4-pin connector	Х3	• 1		
	1: 0 V			
	2: Status S	• 4		
	3: Latch L			
	4: Gate G			

n.c. = not connected

7 Operation

7.1 Messages

A module error is displayed via the "Err" LED, signalled to the head module and then entered in the head module's diagnostic log.

The module can detect the following errors:

Module error	Declaration	Remedy
Start-up error	Error as the PSSu system starts up	Change faulty module.
Configuration error	Incorrect module type configured.	The configured hardware registry does not match the actual hard-ware registry.
ST communication error	Error during ST communication	Change faulty module.
Bus termination error	There is no terminating plate or there is a bad contact with the module bus.	Install a terminating plate with in- tegrated end bracket or insert the base modules together correctly.

7.2 Display elements

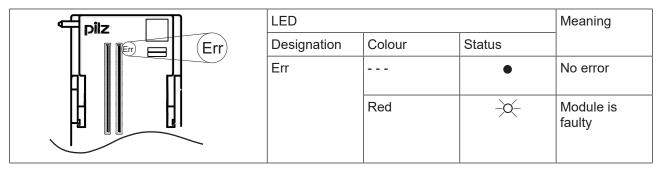
Legend

–o– LED on

LED off

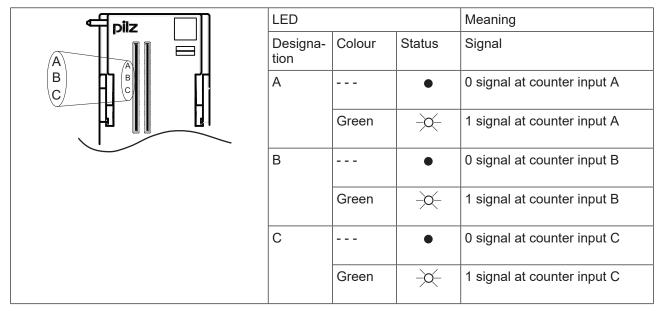
7.2.1 Display elements for module diagnostics

The module has an LED for displaying module errors ("Err" LED).



7.2.2 Display elements for counter status

The module has three LEDs for the status of the counter inputs (LEDs "A", "B" and "C").



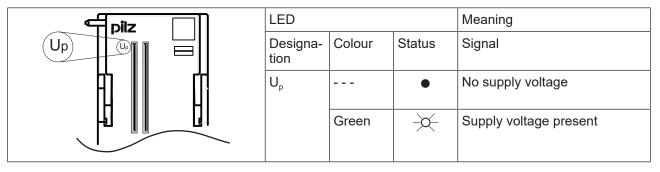
7.2.3 Display elements for status of the functional inputs

The module has three LEDs for the status of the function inputs (LEDs "G", "L" and "S").

fpilz □]	LED			Meaning
	Designa- tion	Colour	Status	Signal
	G		•	0 signal at function input G
		Green	-X-	1 signal at function input G
	L		•	0 signal at function input L
		Green	-×-	1 signal at function input L
	S		•	0 signal at function input S
		Green	->\$\$-	1 signal at function input S

7.2.4 Display elements for sensor supply

The module has an LED for the status of the sensor's supply voltage.



7.3 Status information

The I/O data with status information is described in the chapter entitled "Function Description", under "Input/output data".

8 Technical Details

CertificationsCE, EAC, KOSHA, TÜV, UKCA, cULus ListedApplication rangeFailsafeModule's device code0324hNumber of ST output bits64Number of ST output bits8Application in system environment B from FS firmware version, head modules1.0.0Electrical data1.0.0Electrical data1.0.0Electrical data1.0.0Module's opper consumption0,79 WPertphery's supply voltage (module supply) Voltage range16,8 - 30 VModule's opwer consumption with no load1,37 WModule's opwer consumption with no load0,37 WMacule's opwer consumption with no load0,37 WMax. power dissipation of module1,2 WIncremental encoder inputs1Type of counter inputs1Type of counter inputs1Type input suppla and B and/or CDifferential signal (RS-422/TTL)Permitted how signal range on LATCH/GATE/ STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at low level0 mAMax. number of bits on the counter input32 BitPhase offset tolerance30 degPhase offset toleran	General	
Module's device code 0324h Number of ST input bits 64 Number of ST output bits 32 Number of ST control bits 8 Number of ST control bits 8 Application in system environment B from FS firmware version, head modules 1.0.0 Electrical data Internal supply voltage (module supply) Module's power consumption 0.79 W Periphery's supply voltage (periphery supply) Voltage range Module's current consumption with no load 15 mA Module's power consumption with no load 0.37 W Max. power dissipation of module 1,2 W Incremental encoder input Number of counter inputs Number of counter inputs 1 Type of counter inputs A and B and/or C Differential signal (RS-422/TTL) Permitted high signal range on LATCH/GATE/ 3-5 V StaTUS signals -3 - 5 V Permitted high signal range on LATCH/GATE/ 0 mA Typ. input current of the LATCH and/or GATE and/or 5 Bit Evaluation of counter pulses 1x, 2x, 4x Phase offset between differential signals A and B 90 deg Phase offset between differential signal S A and B 90 deg Phase offset between differential signal 50 µs Time constant of input filter on GATE signal	Certifications	CE, EAC, KOSHA, TÜV, UKCA, cULus Listed
Number of ST input bits 64 Number of ST output bits 32 Number of ST control bits 8 Application in system environment B from FS firmware version, head modules 1.0.0 Electrical data 1.0.0 Internal supply voltage (module supply) Module's power consumption 0,79 W Periphery's supply voltage (periphery supply) Voltage range 16,8 - 30 V Module's power consumption with no load 15 mA Module's power consumption with no load 0,37 W Max. power dissipation of module 1,2 W Incremental encoder input 1 Number of counter inputs 1 Type of counter inputs 1 Signal at counter inputs 1 Signal at counter inputs -3 - 5 V Permitted high signal range on LATCH/GATE/ STATUS signals 0 mA Type. input current of the LATCH and/or GATE and/or STATUS signals at low level 0 mA Typ. input current of the LATCH and/or GATE and/or STATUS signals at low level 0 mA Max. number of bits on the counter input 32 Bit Evaluation of outer pulses 1x, 2x, 4x Phase offset between differential signal SA and B 90 deg Max. number of bits on the COTEH signal 50 µs Time constant of input filter on GATE signal 50 µs Time constant of input filter on GATE	Application range	Failsafe
Number of ST output bits 32 Number of ST status bits 8 Number of ST control bits 8 Application in system environment B from FS firmware version, head modules 1.0.0 Electrical data 1.0.0 Electrical data 1.0.0 Electrical data 0,79 W Periphery's supply voltage (module supply) 0,79 W Voltage range 16,8 - 30 V Module's power consumption with no load 15 mA Module's power consumption with no load 0,37 W Max. power dissipation of module 1,2 W Incernental encoder input 1 Number of counter inputs 1 Type of counter inputs 1 Permitted low signal range on LATCH/GATE/ STATUS signals TATUS signals 11 - 30 V Typ. input current of the LATCH and/or GATE and/or 0 mA Typ. input current of the LATCH and/or GATE and/or STATUS signals at low level Typ. input current of the LATCH and/or GATE and/or STATUS signals at low level Typ. input current of the LATCH and/or GATE and/or STATUS signals at high level Max. number of bits on the counter input 32 Bit Evaluation of counter pulses 1, 2x, 4x Phase offset tolerance 30 deg Maximum cutoff frequency	Module's device code	0324h
Number of ST status bits 8 Number of ST control bits 8 Application in system environment B from FS firmware version, head modules 1.0.0 Electrical data Internal supply voltage (module supply) Module's power consumption 0,79 W Periphery's supply voltage (periphery supply) Voltage range 16,8 - 30 V Module's current consumption with no load 15 mA Module's power consumption with no load Max, power dissipation of module 1,2 W Incremental encoder Incremental encoder input Number of counter inputs 1 Type of counter inputs 1 Type of counter inputs 1 Type of counter inputs 1 Status Signals -3 - 5 V Permitted low signal range on LATCH/GATE/ STATUS signals -3 - 5 V STATUS signals 11 - 30 V Typ. input current of the LATCH and/or GATE and/or STATUS signals at low level 0 mA 0 Typ. input current of the LATCH and/or GATE and/or STATUS signals at low level 0 mA Typ. input current of the LATCH and/or GATE and/or STATUS signals at low level 0 mA Typ. input current of the LATCH and/or GATE and/or STATUS signals at high level 4 mA Max. number of bits on the counter input 32 Bit Evaluation of counter pulse	Number of ST input bits	64
Number of ST control bits 8 Application in system environment B from FS firmware version, head modules 1.0.0 Electrical data 1.0.0 Internal supply voltage (module supply) Module's power consumption 0,79 W Periphery's supply voltage (periphery supply) Voltage range 16,8 - 30 V Module's power consumption with no load 15 mA Module's power consumption with no load 0,37 W Max. power dissipation of module 1,2 W 1 Incremental encoder input 1 1 Number of counter inputs 1 1 Type of counter inputs A and B and/or C Differential signal (RS-422/TTL) Permitted low signal range on LATCH/GATE/ STATUS signals 3 - 5 V Permitted low signal range on LATCH/GATE/ STATUS signals at low level 0 mA 0 7 Typ. input current of the LATCH and/or GATE and/or STATUS signals at low level 0 mA 0 7 Typ. input current of the LATCH and/or GATE and/or STATUS signals at high level 4 mA 4 7 Max. number of bits on the counter input 32 Bit 2 1 1 Phase offset tolerance 30 deg 9 9 9	Number of ST output bits	32
Application in system environment B 1.0.0 From FS firmware version, head modules 1.0.0 Electrical data 1.0.0 Internal supply voltage (module supply) 0,79 W Module's power consumption 0,79 W Periphery's supply voltage (periphery supply) Voltage range 16,8 - 30 V Module's current consumption with no load 0,37 W Max. power dissipation of module 1,2 W Incremental encoder input Number of counter inputs 1 Type of counter inputs 1 Type of counter inputs A and B and/or C Differential signal (RS-422/TTL) Permitted low signal range on LATCH/GATE/ STATUS signals STATUS signals -3 - 5 V Permitted high signal range on LATCH/GATE/ STATUS signals at low level Typ. input current of the LATCH and/or GATE and/or 0 mA Typ. input current of the LATCH and/or GATE and/or STATUS signals at low level Max. number of bits on the counter input 32 Bit Evaluation of counter pulses 1x, 2x, 4x Phase offset between differential signals A and B 90 deg Phase offset tolerance 30 deg Maximum cutoff frequency 5	Number of ST status bits	8
from FS firmware version, head modules 1.0.0 Electrical data Internal supply voltage (module supply) Module's power consumption 0,79 W Periphery's supply voltage (periphery supply) Voltage range Module's current consumption with no load 0,37 W Max. power dissipation of module 1,2 W Incremental encoder input Number of counter inputs Number of counter inputs Incremental encoder Signal at counter inputs A and B and/or C Differential signal (RS-422/TTL) Permitted high signal range on LATCH/GATE/ STATUS signals -3 - 5 V Permitted high signal range on LATCH/GATE/ STATUS signals at low level 0 mA Typ. input current of the LATCH and/or GATE and/or STATUS signals at low level 0 mA Typ. input current of the LATCH and/or GATE and/or STATUS signals at high level 4 mA Max. number of bits on the counter input 32 Bit Evaluation of counter pulses 1x, 2x, 4x Phase offset between differential signals A and B 90 deg Phase offset tolerance 30 deg Maximum cutoff frequency 5 MHz Time constant of input filter on STATUS signal 50 µs Time constant of input filter on STATUS signal	Number of ST control bits	8
Electrical data Internal supply voltage (module supply) Module's power consumption 0,79 W Periphery's supply voltage (periphery supply) Voltage range 16,8 - 30 V Module's current consumption with no load 0,37 W Max. power dissipation of module 1,2 W Incremental encoder input Incremental encoder Number of counter inputs 1 Type of counter inputs A and B and/or C Differential signal (RS-422/TTL) Permitted low signal range on LATCH/GATE/ STATUS signals STATUS signals -3 - 5 V Permitted low signal range on LATCH/GATE/ STATUS signals at low level Typ. input current of the LATCH and/or GATE and/or STATUS signals at low level 0 mA Typ. input current of the LATCH and/or GATE and/or STATUS signals at high level 4 mA Max. number of bits on the counter input 32 Bit Evaluation of counter pulses 1x, 2x, 4x Phase offset between differential signals A and B 90 deg Phase offset tolerance 30 deg Maxinum cutoff frequency 5 MHz Time constant of input filter on CATE signal 50 µs Time constant of input filter on GATE signal	Application in system environment B	
Internal supply voltage (module supply) 0,79 W Module's power consumption 0,79 W Periphery's supply voltage (periphery supply) Voltage range 16,8 - 30 V Module's current consumption with no load 15 mA Module's power consumption with no load 0,37 W Max. power dissipation of module 1,2 W Incremental encoder input Incremental encoder Number of counter inputs 1 Type of counter inputs 1 Permitted low signal range on LATCH/GATE/ Differential signal (RS-422/TTL) Permitted low signals range on LATCH/GATE/ STATUS signals -3 - 5 V Permitted low signals are on LATCH/GATE and/or T1 - 30 V Typ. input current of the LATCH and/or GATE and/or O mA Typ. input current of the LATCH and/or GATE and/or STATUS signals at low level 0 mA Max. number of bits on the counter input 32 Bit Evaluation of counter pulses 1x, 2x, 4x Phase offset between differential signals A and B 90 deg Phase offset between differential signal S A and B 90 usg Maximum cutoff frequency 5 MHz Time constant of input filter on LATCH signal 50 µs Signal 50 µs Time constant of input filter on CATE signal 50 µs	from FS firmware version, head modules	1.0.0
Module's power consumption0,79 WPeriphery's supply voltage (periphery supply)Voltage range16,8 - 30 VModule's current consumption with no load15 mAModule's current consumption with no load15 mAMax. power dissipation of module1,2 WIncremental encoder inputs1Number of counter inputs1Type of counter inputs A and B and/or CDifferential signal (RS-422/TTL)Permitted low signal range on LATCH/GATE/ STATUS signals-3 - 5 VPermitted high signal range on LATCH/GATE/ STATUS signals at low level11 - 30 VTyp. input current of the LATCH and/or GATE and/or STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at high level4 mAMax. number of bits on the counter input32 BitEvaluation of counter input32 BitEvaluation of counter pulses1, 2x, 4xPhase offset between differential signals A and B90 degPhase offset tolerance30 degMaximum cutoff frequency5 MHzTime constant of input filter on LATCH signal50 μsTime constant of input filter on STATUS signal50 μsTime constant of input filter on STATUS signal50 μsTime constant of input filter on STATUS signal50 μsTyp. processing time0,1 msPotential isolation between input/output and voltageYes	Electrical data	
Periphery's supply voltage (periphery supply) 16,8 - 30 V Voltage range 16,8 - 30 V Module's current consumption with no load 0,37 W Max. power dissipation of module 1,2 W Incremental encoder input Number of counter inputs Number of counter inputs 1 Type of counter inputs Incremental encoder Signal at counter inputs A and B and/or C Differential signal (RS-422/TTL) Permitted low signal range on LATCH/GATE/ 5 V STATUS signals -3 - 5 V Permitted high signal range on LATCH/GATE/ 5TATUS signals at low level Typ. input current of the LATCH and/or GATE and/or 0 mA STATUS signals at low level 0 mA Typ. input current of the LATCH and/or GATE and/or 5 MHZ STATUS signals at high level 4 mA Max. number of bits on the counter input 32 Bit Evaluation of counter pulses 1x, 2x, 4x Phase offset tolerance 30 deg Maximum cutoff frequency 5 MHz Time constant of input filter on LATCH signal 50 µs Time constant of input filter on STATUS signal 50 µs Time constant of input filter on GATE sign	Internal supply voltage (module supply)	
Voltage range16,8 - 30 VModule's current consumption with no load15 mAModule's power consumption with no load0,37 WMax. power dissipation of module1,2 WIncremental encoder input1Number of counter inputs1Type of counter inputs1Signal at counter inputs A and B and/or CDifferential signal (RS-422/TTL)Permitted low signal range on LATCH/GATE/ STATUS signals-3 - 5 VPermitted high signal range on LATCH/GATE/ STATUS signals11 - 30 VTyp. input current of the LATCH and/or GATE and/or STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at high level4 mAMax. number of bits on the counter input32 BitEvaluation of counter pulses1x, 2x, 4xPhase offset between differential signal A and B90 degPhase offset tolerance30 degMaximum cutoff frequency5 MHzTime constant of input filter on STATUS signal50 µsTyp. processing time0,1 msPotential isolation between input/output and voltageYes	Module's power consumption	0,79 W
Module's current consumption with no load15 mA 0,37 WMax. power dissipation of module1,2 WIncremental encoder input1Number of counter inputs1Type of counter inputs1Signal at counter inputs A and B and/or CDifferential signal (RS-422/TTL)Permitted low signal range on LATCH/GATE/ STATUS signals-3 - 5 VPermitted high signal range on LATCH/GATE/ STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at high level4 mAMax. number of bits on the counter input32 BitEvaluation of counter pulses1x, 2x, 4xPhase offset between differential signal SA and B90 degPhase offset tolerance30 degMaximum cutoff frequency5 MHzTime constant of input filter on LATCH signal50 µsTime constant of input filter on STATUS signal50 µsPotential is	Periphery's supply voltage (periphery supply)	
Module's power consumption with no load0,37 WMax. power dissipation of module1,2 WIncremental encoder input1Number of counter inputs1Type of counter inputs A and B and/or CDifferential signal (RS-422/TTL)Permitted low signal range on LATCH/GATE/ STATUS signals-3 - 5 VPermitted high signal range on LATCH/GATE/ STATUS signals-3 - 5 VPermitted high signal range on LATCH/GATE/ STATUS signals11 - 30 VTyp. input current of the LATCH and/or GATE and/or STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at low level2 BitEvaluation of counter pulses1x, 2x, 4xPhase offset between differential signals A and B90 degPhase offset between differential signals A and B90 degPhase offset tolerance30 degMaximum cutoff frequency5 MHzTime constant of input filter on LATCH signal50 µsTime constant of input filter on STATUS signal50 µsTime constant of input	Voltage range	16,8 - 30 V
Max. power dissipation of module1,2 WIncremental encoder input1Number of counter inputs1Type of counter inputs A and B and/or CDifferential encoderSignal at counter inputs A and B and/or CDifferential signal (RS-422/TTL)Permitted low signal range on LATCH/GATE/ STATUS signals-3 - 5 VPermitted high signal range on LATCH/GATE/ STATUS signals at low level11 - 30 VTyp. input current of the LATCH and/or GATE and/or STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at high level4 mAMax. number of bits on the counter input32 BitEvaluation of counter pulses1x, 2x, 4xPhase offset between differential signals A and B90 degPhase offset tolerance30 degMaximum cutoff frequency5 MHzTime constant of input filter on LATCH signal50 μsTime constant of input filter on STATUS signal50 μsTyp. processing time0,1 msPotential isolation between input/output and periphery supplyYesPotential isolation between input/output and voltage	Module's current consumption with no load	15 mA
Incremental encoder inputNumber of counter inputs1Type of counter inputsIncremental encoderSignal at counter inputs A and B and/or CDifferential signal (RS-422/TTL)Permitted low signal range on LATCH/GATE/ STATUS signals-3 - 5 VPermitted high signal range on LATCH/GATE/ STATUS signals11 - 30 VTyp. input current of the LATCH and/or GATE and/or STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at high level4 mAMax. number of bits on the counter input32 BitEvaluation of counter pulses1x, 2x, 4xPhase offset between differential signals A and B90 degPhase offset betrance30 degMaximum cutoff frequency5 MHzTime constant of input filter on LATCH signal50 µsTime constant of input filter on STATUS signal50 µsTime constant of input filter on STATUS signal50 µsPotential isolation between input/output and periphery supplyYesPotential isolation between input/output and voltageYes		0,37 W
Number of counter inputs1Type of counter inputsIncremental encoderSignal at counter inputs A and B and/or CDifferential signal (RS-422/TTL)Permitted low signal range on LATCH/GATE/ STATUS signals-3 - 5 VPermitted high signal range on LATCH/GATE/ STATUS signals11 - 30 VTyp. input current of the LATCH and/or GATE and/or STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at high level4 mAMax. number of bits on the counter input32 BitEvaluation of counter pulses1x, 2x, 4xPhase offset between differential signals A and B90 degPhase offset tolerance30 degMaximum cutoff frequency5 MHzTime constant of input filter on LATCH signal50 μsTime constant of input filter on STATUS signal50 μsTyp. processing time0,1 msPotential isolation between input/output and periphery supplyYesPotential isolation between input/output and voltage	Max. power dissipation of module	1,2 W
Type of counter inputsIncremental encoderSignal at counter inputs A and B and/or CDifferential signal (RS-422/TTL)Permitted low signal range on LATCH/GATE/ STATUS signals-3 - 5 VPermitted high signal range on LATCH/GATE/ STATUS signals11 - 30 VTyp. input current of the LATCH and/or GATE and/or STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at high level4 mAMax. number of bits on the counter input32 BitEvaluation of counter pulses1x, 2x, 4xPhase offset between differential signals A and B90 degPhase offset tolerance30 degMaximum cutoff frequency5 MHzTime constant of input filter on LATCH signal50 µsTime constant of input filter on STATUS signal50 µsTyp. processing time0,1 msPotential isolation between input/output and voltage	Incremental encoder input	
Signal at counter inputs A and B and/or C Differential signal (RS-422/TTL) Permitted low signal range on LATCH/GATE/ STATUS signals -3 - 5 V Permitted high signal range on LATCH/GATE/ STATUS signals at low level 0 mA Typ. input current of the LATCH and/or GATE and/or STATUS signals at low level 0 mA Typ. input current of the LATCH and/or GATE and/or STATUS signals at high level 4 mA Max. number of bits on the counter input 32 Bit Evaluation of counter pulses 1x, 2x, 4x Phase offset between differential signals A and B 90 deg Phase offset tolerance 30 deg Maximum cutoff frequency 5 MHz Time constant of input filter on LATCH signal 50 µs Time constant of input filter on STATUS signal 50 µs Typ. processing time 0,1 ms Potential isolation between input/output and voltage	Number of counter inputs	1
Permitted low signal range on LATCH/GATE/ STATUS signals -3 - 5 V Permitted high signal range on LATCH/GATE/ STATUS signals 11 - 30 V Typ. input current of the LATCH and/or GATE and/or STATUS signals at low level 0 mA Typ. input current of the LATCH and/or GATE and/or STATUS signals at low level 0 mA Typ. input current of the LATCH and/or GATE and/or STATUS signals at high level 4 mA Max. number of bits on the counter input 32 Bit Evaluation of counter pulses 1x, 2x, 4x Phase offset between differential signals A and B 90 deg Phase offset tolerance 30 deg Maximum cutoff frequency 5 MHz Time constant of input filter on LATCH signal 50 µs Time constant of input filter on STATUS signal 50 µs Time constant of input filter on STATUS signal 50 µs Time constant of input filter on STATUS signal 50 µs Time constant of input filter on STATUS signal 50 µs Typ. processing time 0,1 ms Potential isolation between input/output and periphery supply Yes Potential isolation between input/output and voltage Yes	Type of counter inputs	Incremental encoder
STATUS signals-3 - 5 VPermitted high signal range on LATCH/GATE/ STATUS signals11 - 30 VTyp. input current of the LATCH and/or GATE and/or STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at high level4 mAMax. number of bits on the counter input32 BitEvaluation of counter pulses1x, 2x, 4xPhase offset between differential signals A and B90 degPhase offset tolerance30 degMaximum cutoff frequency5 MHzTime constant of input filter on LATCH signal50 µsTime constant of input filter on STATUS signal50 µsTyp. processing time0,1 msPotential isolation between input/output and periphery supplyYesPotential isolation between input/output and voltage		Differential signal (RS-422/TTL)
STATUS signals11 - 30 VTyp. input current of the LATCH and/or GATE and/or STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at high level4 mAMax. number of bits on the counter input32 BitEvaluation of counter pulses1x, 2x, 4xPhase offset between differential signals A and B90 degPhase offset tolerance30 degMaximum cutoff frequency5 MHzTime constant of input filter on LATCH signal50 µsTime constant of input filter on STATUS signal50 µsTime constant of input filter on STATUS signal50 µsTyp. processing time0,1 msPotential isolation between input/output and voltageYes		-3 - 5 V
STATUS signals at low level0 mATyp. input current of the LATCH and/or GATE and/or STATUS signals at high level4 mAMax. number of bits on the counter input32 BitEvaluation of counter pulses1x, 2x, 4xPhase offset between differential signals A and B90 degPhase offset tolerance30 degMaximum cutoff frequency5 MHzTime constant of input filter on LATCH signal50 µsTime constant of input filter on STATUS signal50 µsTime constant of input filter on STATUS signal50 µsTyp. processing time0,1 msPotential isolation between input/output and periphery supplyYesPotential isolation between input/output and voltage		11 - 30 V
STATUS signals at high level4 mAMax. number of bits on the counter input32 BitEvaluation of counter pulses1x, 2x, 4xPhase offset between differential signals A and B90 degPhase offset tolerance30 degMaximum cutoff frequency5 MHzTime constant of input filter on LATCH signal50 µsTime constant of input filter on GATE signal50 µsTime constant of input filter on STATUS signal50 µsTyp. processing time0,1 msPotential isolation between input/output and periphery supplyYesPotential isolation between input/output and voltage		0 mA
Evaluation of counter pulses1x, 2x, 4xPhase offset between differential signals A and B90 degPhase offset tolerance30 degMaximum cutoff frequency5 MHzTime constant of input filter on LATCH signal50 µsTime constant of input filter on GATE signal50 µsTime constant of input filter on STATUS signal50 µsTyp. processing time0,1 msPotential isolation between input/output and periphery supplyYesPotential isolation between input/output and voltage		4 mA
Phase offset between differential signals A and B90 degPhase offset tolerance30 degMaximum cutoff frequency5 MHzTime constant of input filter on LATCH signal50 µsTime constant of input filter on GATE signal50 µsTime constant of input filter on STATUS signal50 µsTyp. processing time0,1 msPotential isolation between input/output and periphery supplyYesPotential isolation between input/output and voltage	Max. number of bits on the counter input	32 Bit
Phase offset tolerance30 degMaximum cutoff frequency5 MHzTime constant of input filter on LATCH signal50 µsTime constant of input filter on GATE signal50 µsTime constant of input filter on STATUS signal50 µsTyp. processing time0,1 msPotential isolation between input/output and periphery supplyYesPotential isolation between input/output and voltage	Evaluation of counter pulses	1x, 2x, 4x
Maximum cutoff frequency5 MHzTime constant of input filter on LATCH signal50 µsTime constant of input filter on GATE signal50 µsTime constant of input filter on STATUS signal50 µsTyp. processing time0,1 msPotential isolation between input/output and periphery supplyYesPotential isolation between input/output and voltage	Phase offset between differential signals A and B	90 deg
Time constant of input filter on LATCH signal 50 µs Time constant of input filter on GATE signal 50 µs Time constant of input filter on STATUS signal 50 µs Typ. processing time 0,1 ms Potential isolation between input/output and periphery supply Yes Potential isolation between input/output and voltage	Phase offset tolerance	30 deg
Time constant of input filter on GATE signal50 µsTime constant of input filter on STATUS signal50 µsTyp. processing time0,1 msPotential isolation between input/output and periphery supplyYesPotential isolation between input/output and voltage	Maximum cutoff frequency	5 MHz
Time constant of input filter on STATUS signal 50 µs Typ. processing time 0,1 ms Potential isolation between input/output and periphery supply Yes Potential isolation between input/output and voltage	Time constant of input filter on LATCH signal	50 µs
Typ. processing time 0,1 ms Potential isolation between input/output and periphery supply Yes Potential isolation between input/output and voltage	Time constant of input filter on GATE signal	50 µs
Potential isolation between input/output and periphery supply Yes Potential isolation between input/output and voltage	Time constant of input filter on STATUS signal	50 µs
supply Yes Potential isolation between input/output and voltage	Typ. processing time	0,1 ms
		Yes

Environmental data	
Climatic suitability	EN 60068-2-1, EN 60068-2-14, EN 60068-2-2, EN 60068-2-30, EN 60068-2-78
Ambient temperature	
in accordance with the standard	EN 60068-2-14
Temperature range	0 - 60 °C
Storage temperature	
in accordance with the standard	EN 60068-2-1/-2
Temperature range	-40 - 70 °C
Climatic suitability	
in accordance with the standard	EN 60068-2-78
Humidity	93 % r. h. at 40 °C
Condensation during operation	Not permitted
Max. operating height above SL	2000 m
EMC	EN 12015, EN 12016, EN 61000-6-2, EN 61000-6-4, EN 61131-2
Vibration	
in accordance with the standard	EN 60068-2-6
Frequency	10 - 150 Hz
Acceleration	10 m/s²
Shock stress	
in accordance with the standard	EN 60068-2-27
Number of shocks	6
Acceleration	150 m/s²
Duration	11 ms
Airgap creepage	
in accordance with the standard	EN 61131-2
Overvoltage category	II
Pollution degree	2
Protection type	
in accordance with the standard	EN 60529
Housing	IP20
Mounting area (e.g. control cabinet)	IP54
Mechanical data	
Material	
Bottom	PC
Тор	PC
Connection type	D-Sub female connector, D-Sub male connector
Dimensions	
Height	128,9 mm
Width	30 mm
Depth	56 mm
Weight	90 g

Where standards are undated, the 2022-01 latest editions shall apply.

8.1 Safety characteristic data



NOTICE

You must comply with the safety characteristic data in order to achieve the required safety level for your plant/machine.

Operating mode	EN ISO 13849-1: 2015 PL	EN ISO 13849-1: 2015 Category	EN IEC 62061 SIL CL/ maximum SIL	EN IEC 62061 PFH _D [1/h]	EN/IEC 61511 SIL	EN/IEC 61511 PFD	EN ISO 13849-1: 2015 T _M [year]
-	PL e	Cat. 4	SIL CL 3	3,34E-09	SIL 3	5,62E-06	20

Explanatory notes for the safety-related characteristic data:

- Safety characteristic data in accordance with EN IEC 62061 and EN/IEC 61511 was calculated based on EN/IEC 61508.
- ▶ T_M is the maximum mission time in accordance with EN ISO 13849-1. The value also applies as the retest interval in accordance with EN/IEC 61508-6 and EN/IEC 61511 and as the proof test interval and mission time in accordance with EN IEC 62061.

All the units used within a safety function must be considered when calculating the safety characteristic data.



INFORMATION

A safety function's SIL/PL values are **not** identical to the SIL/PL values of the units that are used and may be different. We recommend that you use the PAScal software tool to calculate the safety function's SIL/PL values.

9 Order reference

9.1 Product

Product type	Features	Order no.
PSSu K F INC	Compact module without connector, labelling bracket and la- belling strips	312437

9.2 Accessories

Terminals

Product type	Features	Order no.
PSSu A Con 4 S	Connector with screw terminals 4pin	313117
PSSu A Con 4 C	Connector with spring-loaded terminals 4pin	313118

Labelling

Product type	Features	Order no.
PSSu A LC 0.1	Labelling bracket, scope of delivery: 5 pieces	312966
PSSu A LA0	Labelling strips, laser printable, scope of delivery: 1080 pieces (10 x DIN A4 sheet, 108 on each)	312958

10 EC declaration of conformity

This product/these products meet the requirements of the directive 2006/42/EC for machinery of the European Parliament and of the Council. The complete EC Declaration of Conformity is available on the Internet at www.pilz.com/downloads.

Authorised representative: Norbert Fröhlich, Pilz GmbH & Co. KG, Felix-Wankel-Str. 2, 73760 Ostfildern, Germany

11 UKCA-Declaration of Conformity

This product(s) complies with following UK legislation: Supply of Machinery (Safety) Regulation 2008.

The complete UKCA Declaration of Conformity is available on the Internet at www.pilz.com/ downloads.

Representative: Pilz Automation Technology, Pilz House, Little Colliers Field, Corby, Northamptonshire, NN18 8TJ United Kingdom, eMail: mail@pilz.co.uk

Support

Technical support is available from Pilz round the clock.

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Pilz develops environmentally-friendly products using ecological materials and energy-saving technologies. Offices and production facilities are ecologically designed, environmentally-aware and energy-saving. So Pilz offers sustainability, plus the security of using energy-efficient products and environmentally-friendly solutions.









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